EAST Search History

09/919,372

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	phase detect$3 and (first clock circuit same second clock circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:09
L3	5377	detect$3 and (phase different$1 or phase error$1 and (first clock circuit with second clock circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:07
L4	1	l3 and l1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L5	35601	phase detector$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L6	199566	clock signal$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L7	21539	clock circuit$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L8	9775	l6 and l7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L9	682	l5 and l8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08

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L10	28	I9 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:08
L11	120932	error signal$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:09
L12	4	I1 and I11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:09
L13	9925	generat$3 and (first clock signal$1 or second clock signal$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:19
L14	3	I12 and I13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:09
L15	3	I13 and I0	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 10:10
L16	2	I11 and I15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:11
L17	9660	714/798 or 714/707 or 714/700 or 714/814 or 713/500 or 713/503 or 714/699 or 327/158 or 331/2 or 331/16 or 331/17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:18

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L18	806	I11 and I13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:18
L19	102	I17 and I18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:18
L20	150	generat$3 and (first clock signal$1 same error signal$1 or second clock signal$1 same error signal$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:19
L21	49	I20 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:19
L22	2	I21 and (first clock circuit or second clock circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:20
L23	11	I21 and I17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/30 11:22